

J1 = 1/4" phone jack w / n.o. sw (isolated)

Notes:

S1 (pickup selector) is shown in middle position
sequence is Neck, Bridge, Both (out of phase)

S2 (notch filter) is shown in notch position
2N5459 FETs have interchangeable source and
drain: be careful if substituting

all fixed resistors are 1%
except R8, R9, R11 & R12 = 2%

R1 = 100k trimmer

R2 = 10k trimmer

R3 = 150k

R4 = 681k

R5 = 100k

R6, R7 = 221k

R8 = 180k

R9 = 1.5k

R10a = 500k linear

R10b = 500k log

R11 = 470k

R12 = 22k

R13 = 17.8k

R14 = 61.9k

R15 = 100k linear

C1 = 100pf

C2 = .1mf

C3, C7, C8

= 10mf / 25v

C4 = .22mf

C5 = .0056mf

C6 = 39pf

Q1 = 2N5459

Q2 = 2N5459

B1 = 9v battery

B2 = 9v battery

S1 = 4P3T on/on/on

S2 = DPDT,

sections in parallel

H1 = ???? center tapped

torroid with 2 orange

drop caps, it's epoxy

dipped, so value unknown

