

J1 = 1/4" phone jack w/ n.o. sw (isolated)

S1 = 4P3T on/on/on

S2 = DPDT (sections in parallel)

Notes:

S1 (pickup selector switch) shown in middle position

switching sequence is Neck, Bridge, Both (out of phase)

S2 (notch filter switch) shown in notch position

2n5459 FETs have interchangeable source and drain

be careful if substituting

All fixed resistor are 1%

